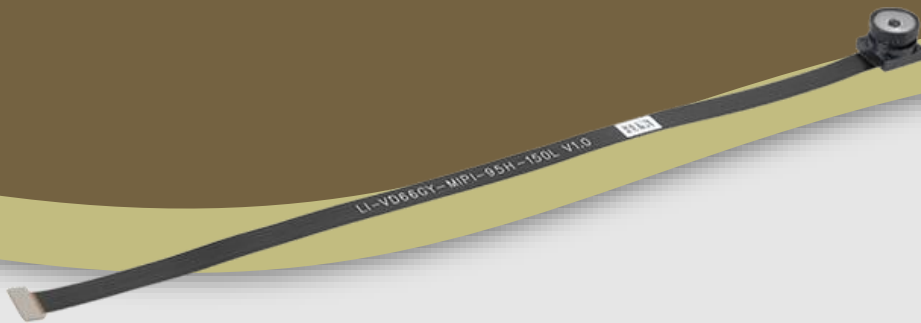




LEOPARD
IMAGING

LI-VD66GY-MIPI-95H-150L



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INTRODUCTION

The LI-VD66GY-MIPI-95H-150L is equipped with STMicroelectronics 1.5MP color Bayer global shutter image sensor VD66GY. It features BSI+DTI technology and integrated image correction functions, delivering superior sensitivity and sharpness. This camera outputs RAW data via MIPI CSI-2, making it ideal for flexible integration with embedded vision platforms.

SPECIFICATIONS

Maximum Frame Rate	88 fps @ full resolution 121 fps @ 1MP resolution 237 fps @ VGA resolution
Output Format	8-bit / 10-bit RAW
Focusing Range	15 cm ~ Infinity
Sensor	STMicroelectronics 1.5MP color Bayer image sensor VD66GY
Color / Mono	Color sensor
Optical Format	4.6 mm (1/4") at full resolution and 3.7 mm (1/5") with 1 MP crop
Pixel Size	2.61 μ m BSI
Shutter Type	Global shutter
Resolution	1124 (H) x 1364 (V)
Interface	2-lane MIPI D-PHY
Power Consumption	TBD
Operating Temp	TBD
Storage Temp	TBD
Weight	~ 1 g

APPLICATIONS

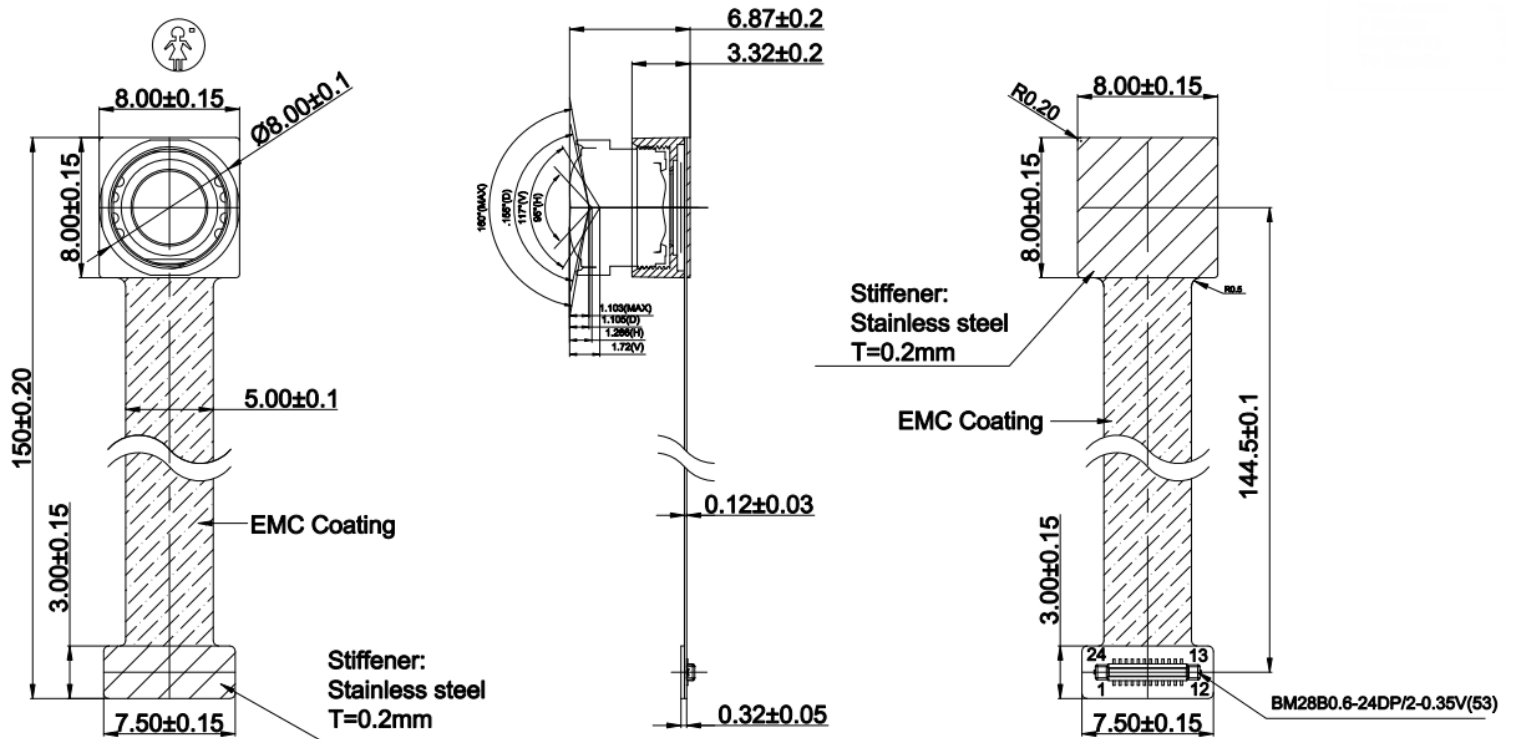
- Robotics
- Smart Home & Appliances
- AR/VR
- Drone
- Healthcare
- Industrial Automation
- 3D Stereo Vision
- Interactive Kiosks

LENS SPECIFICATIONS

Focal Length	1.725 mm
Aperture, F/#	2.0 $\pm$ 5 %
FOV	155° diagonal
	95° horizontal
	117° vertical
TV Distortion	< -22.9 %
Filter Type	650nm IR cut filter



DIMENSIONS



Unit: mm

MODULE CONNECTOR

■ Connector Part#:

BM28B0.6-24DP/2-0.35V(53)

■ Number of Positions: 24

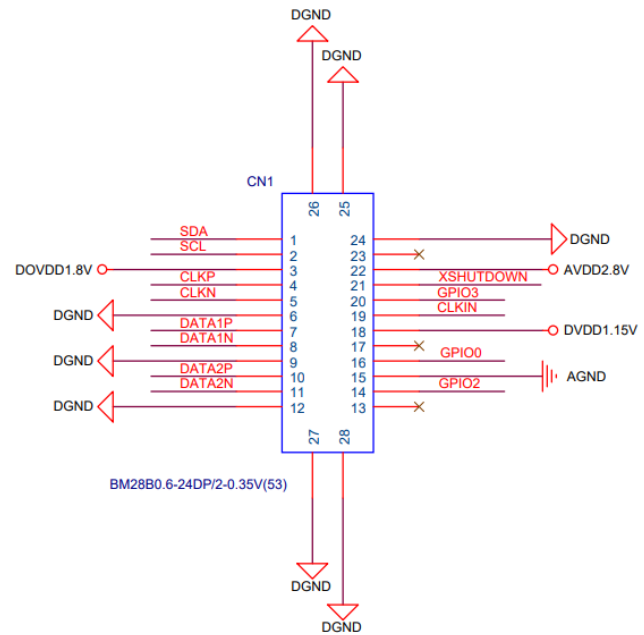
■ Pitch: 0.35 mm

■ Sensor I2C Address:

(W/R): 0X20 / 0X21 (8-bit);

■ External Power Supply: 1.15V, 1.8V, 2.8V

Note: External power supply can rise in any order.



PINOUT DETAILS

Pin No	Signal Name	Pin Type	Description	Voltage Level
1	SDA	I/O	1.8V IO Camera I2C SDA signal (Require pull-up at the host)	1.8 V
2	SCL	INPUT	1.8V IO Camera I2C SCL signal (Require pull-up at the host)	1.8 V
3	DOVDD1.8V	POWER	1.8V I/O Power Supply (Must be provided from the host side)	1.8 V
4	CLKP	OUTPUT	MIPI Clock Lane Differential Pair +	MIPI DPHY
5	CLKN	OUTPUT	MIPI Clock Lane Differential Pair -	MIPI DPHY
6	DGND	POWER	Ground signal for digital	–
7	DATA1P	OUTPUT	MIPI Data Lane 1 Differential Pair +	MIPI DPHY
8	DATA1N	OUTPUT	MIPI Data Lane 1 Differential Pair -	MIPI DPHY
9	DGND	POWER	Ground signal for digital	–
10	DATA2P	OUTPUT	MIPI Data Lane 2 Differential Pair +	MIPI DPHY
11	DATA2N	OUTPUT	MIPI Data Lane 2 Differential Pair -	MIPI DPHY
12	DGND	POWER	Ground signal for digital	–
13	-	–	–	–

Pin No	Signal Name	Pin Type	Description	Voltage Level
14	GPIO2	I/O	General Purpose I/O	1.8 V
15	AGND	POWER	Ground signal for analog	–
16	GPIO0	I/O	General Purpose I/O	1.8 V
17	-	-	-	-
18	DVDD1.15V	POWER	1.15V Digital Power Supply (Must be provided from the host side)	1.15 V
19	XCLK	INPUT	Sensor clock signal (Must be provided from the host side)	1.8 V
20	GPIO3	I/O	General Purpose I/O	1.8 V
21	XSHUTDOWN	INPUT	Reset (Active low)	1.8 V
22	AVDD2.8V	POWER	2.8V Analog Power Supply (Must be provided from the host side)	2.8 V
23	–	–	–	–
24	DGND	POWER	Ground signal for digital	–

● REVISION HISTORY

Revision	Description	Release Date
0.1	Initial draft.	23 Jul 2026

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