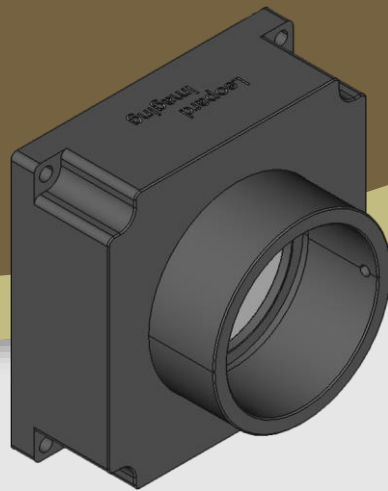




LEOPARD
IMAGING

LI-IMX565-MIPI-FKR-IPX-NL



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INTRODUCTION

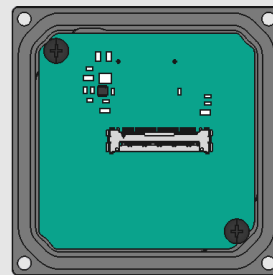
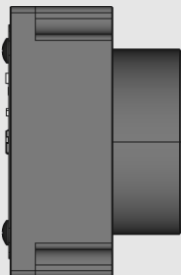
The LI-IMX565-MIPI-FKR-IPX-NL is a stack camera. It's equipped with Sony diagonal 14 mm (Type 1/1.1) Pregius CMOS image sensor IMX565 which features a global shutter. Assembled with no lens, this camera outputs 8-bit / 10-bit / 12-bit RAW data.

SPECIFICATIONS

Sensor	Sony Diagonal 14 mm CMOS Image Sensor IMX565
Optical Format	1/1.1 "
Resolution	4128 (H) x 3008 (V) (active pixels)
Pixel Size	2.74 x 2.74 μm
Output Format	8-bit / 10-bit / 12-bit RAW data
Color / Mono	Color Sensor
Shutter	Global shutter
Maximum Frame Rate	8-bit: 42.6 fps @ All-pixel scan mode 10-bit: 34.6 fps @ All-pixel scan mode 12-bit: 29.2 fps @ All-pixel scan mode
Interface	4-lane MIPI CSI-2
Lens	Not included
Lens Mount	C-mount
Power Consumption	TBD
Operating Temp	TBD
Storage Temp	TBD
Weight	TBD
Part#	LI-IMX565-MIPI-FKR-IPX-NL

APPLICATIONS

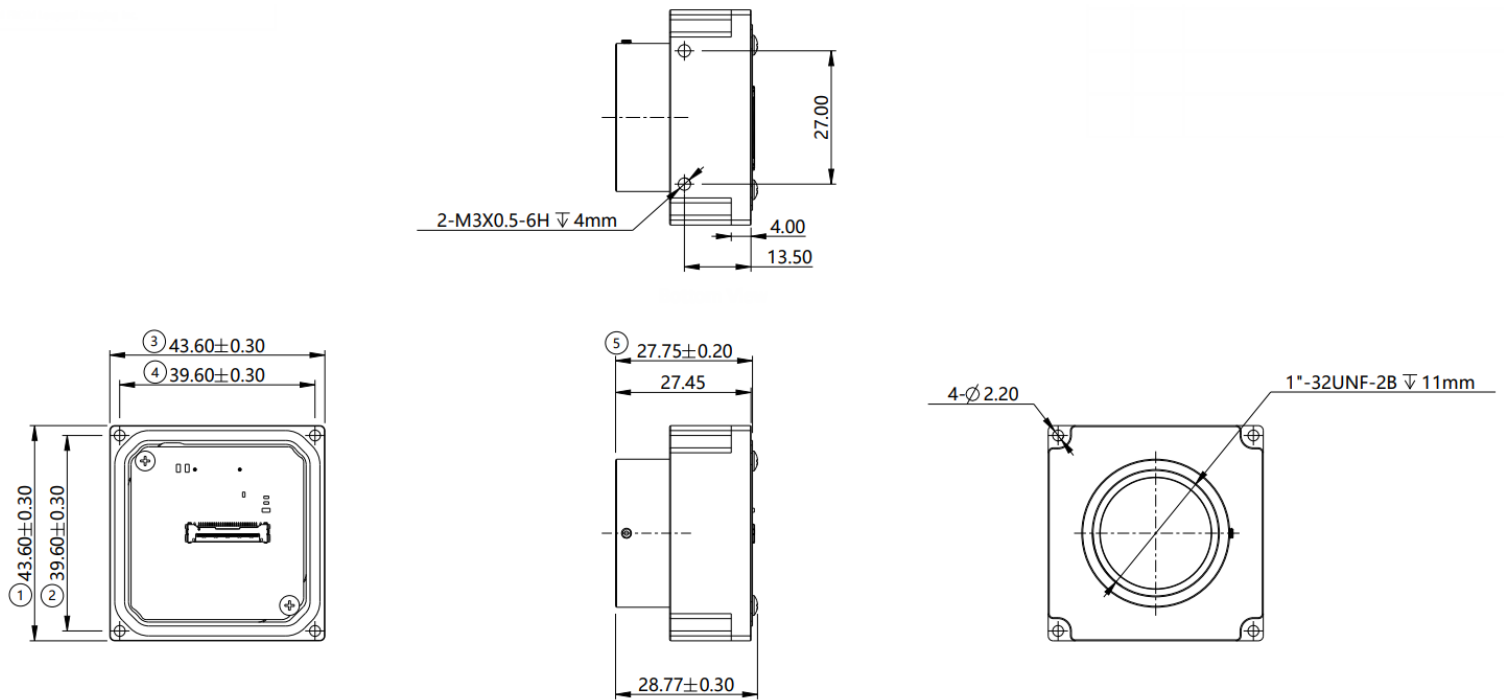
- ITS Cameras
- FA Cameras



NOTE:

Lens is not included in LI-IMX565-MIPI-FKR-IPX-NL.

DIMENSIONS (Without Lens)

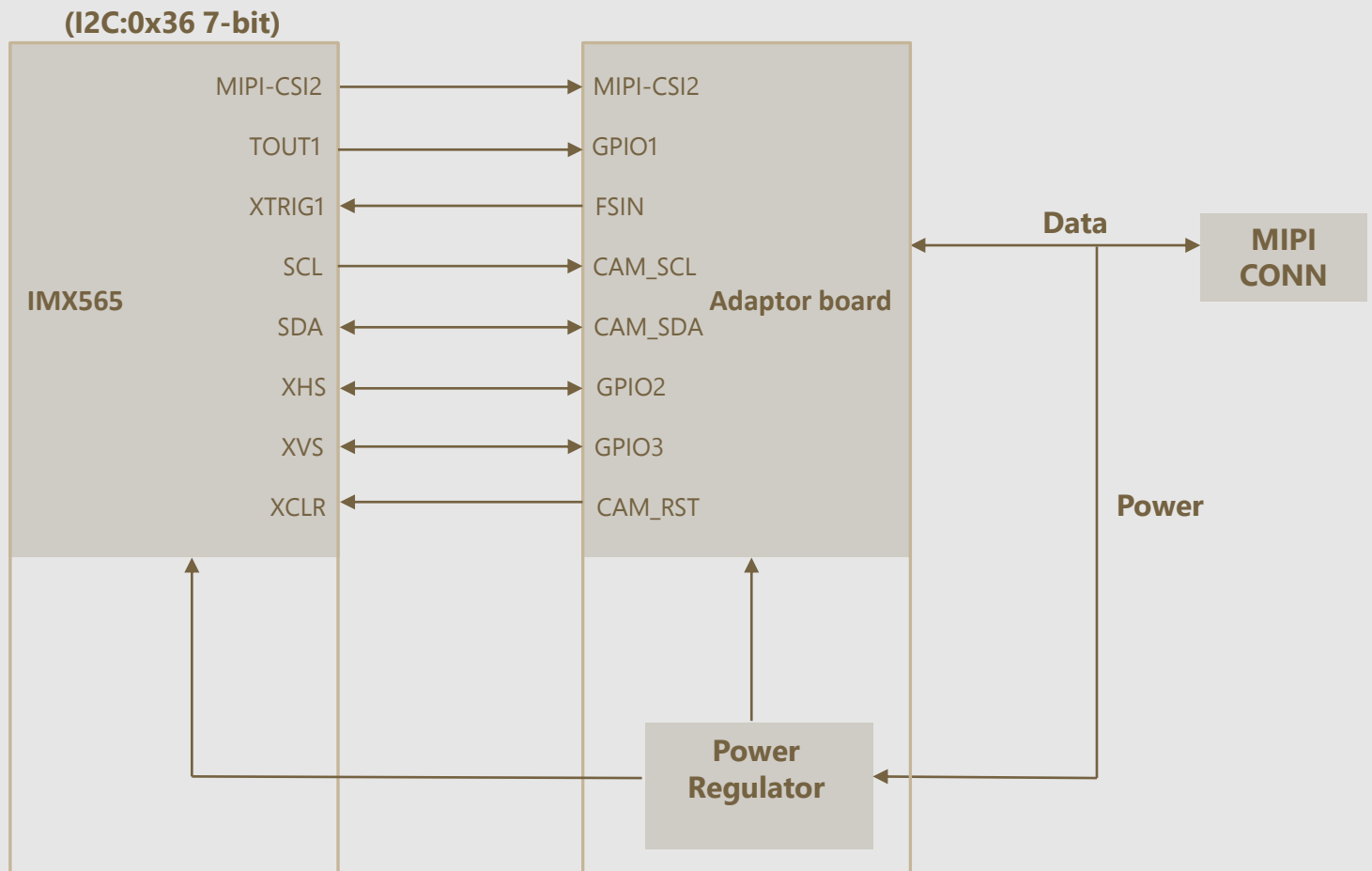


NOTE:

- ⊗ marked are important sizes.
- Tolerances for others unmarked – refer to the Tolerance table.
- All materials are compliant with RoHS requirements.
- Unit: mm

TOLERANCE TABLE					
LENGTH TOLERANCE		CHAMFER TOLERANCE		ANGLE TOLERANCE	
Size X	Tolerance	Size X	Tolerance	Size X	Tolerance
0.5 < X ≤ 3	±0.1	0.5 < X ≤ 3	±0.2	X ≤ 10	±1°
3 < X ≤ 6	±0.1	3 < X ≤ 6	±0.5	10 < X ≤ 50	±30'
6 < X ≤ 30	±0.2	6 < X ≤ 30	±1	50 < X ≤ 120	±20'
30 < X ≤ 120	±0.3	X > 30	±2	120 < X ≤ 400	±10'
120 < X ≤ 400	±0.5			X > 400	±5'
400 < X ≤ 1000	±0.8				
X > 1000	±1.2				

SYSTEM BLOCK DIAGRAM



Pin No	Signal Name	Pin Type	Description	Voltage Level
14	CAM_SDA	I/O	1.8V IO Camera I2C SDA signal with 1.8k Ω pull-up.	1.8V
15	CAM_RST	INPUT	1.8V IO Camera reset signal (Active Low)	1.8V
16	CLK_OUT	INPUT	Reserved CLK for camera	1.8V
17	GPIO1	INPUT	Pluse1 output signal	1.8V
18	FSIN	INPUT	Trigger input signal	1.8V
19	GPIO2	I/O	Horizontal sync signal	1.8V
20	-	-	-	-
21	CAM_XVS	I/O	Vertical sync signal (Reserved)	1.8V
22	-	-	-	-
23	V_1.2V	POWER	Not Used	-
24	V_1.2V	POWER	Not Used	-
25	V_1.8V	POWER	Not Used	-
26	2V8A	POWER	Not Used	-
27	5V0	POWER	Not Used	-
28	3V3	POWER	3.3V power supply	3.3V
29	3V3	POWER	3.3V power supply	3.3V
30	3V3	POWER	3.3V power supply	3.3V

- REVISION HISTORY

Revision	Description	Release Date
0.1	Initial draft.	14 Jul 2025

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