



LEOPARD
IMAGING

LI-GMSL3-USB3.2



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INTRODUCTION

The LI-GMSL3-USB3.2 is a USB3.2 Frame Grabber box, supporting up to 2x GMSL3 cameras. It's equipped with USB3.2 GEN2 controller which integrated 10Gbps SuperSpeed and Maxim GMSL3 DeSerializer MAX96792A.

TECHNICAL FEATURES

- USB 3.2 GEN2 Type C 10Gbps SuperSpeed support
- Supports up to 2 Leopard Imaging GMSL3 FAKRA cameras
- Supports external 12 VDC power supply
- DeSerializer: Maxim MAX96792A
- Operating temperature: TBD
- IP Protection: N/A
- Weight: ~ 196 g
- Part#: **LI-GMSL3-USB3.2**



BOM DETAILS

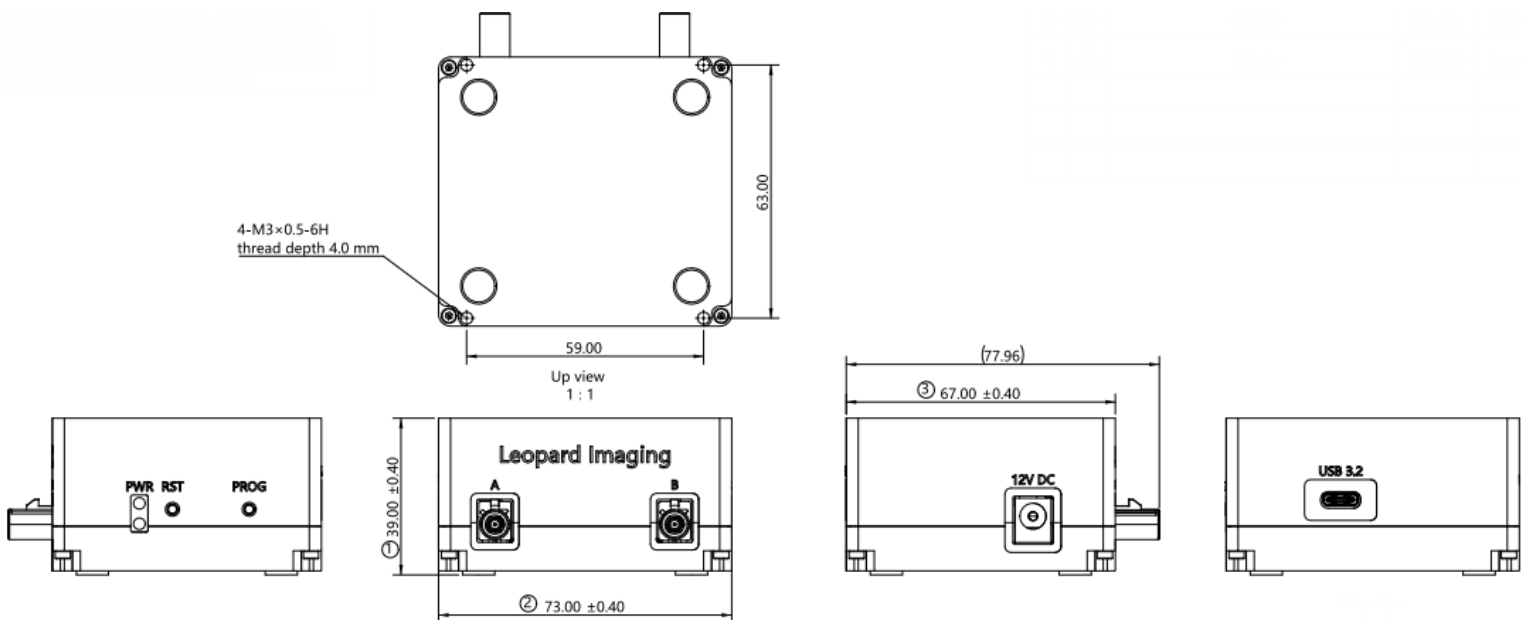
#	Items	QTY
1	LI-GMSL3-USB3.2	1
2	12 VDC power supply	1

SUPPORTED FAKRA CABLE

LI-GMSL3-USB3.2 BOX could connect to the GMSL3 camera modules with the following FAKRA cables:

#	Items	Length	Website Address
1	FAK-SMZSMZ-6M	6 m	https://leopardimaging.com/product/accessories/cables/fak-smzsmz-6m/
2	FAK-SMZSMZ-5M	5 m	https://leopardimaging.com/product/accessories/cables/fak-smzsmz-5m/
3	LI-FCB-1T1-SS-3M-WP-S0	3 m	https://leopardimaging.com/product/accessories/cables/fak-smzsmz/

DIMENSIONS



NOTE:

- ⊗ marked are important sizes.
- Tolerances for the unmarked refer to the Tolerance table.
- All materials are compliant with RoHS requirements.
- Unit: mm

TOLERANCE TABLE					
LENGTH TOLERANCE		CHAMFER TOLERANCE		ANGLE TOLERANCE	
Size X	Tolerance	Size X	Tolerance	Size X	Tolerance
0.5 < X ≤ 3	±0.1	0.5 < X ≤ 3	±0.2	X ≤ 10	±1°
3 < X ≤ 6	±0.1	3 < X ≤ 6	±0.5	10 < X ≤ 50	±30'
6 < X ≤ 30	±0.2	6 < X ≤ 30	±1	50 < X ≤ 120	±20'
30 < X ≤ 120	±0.3	X > 30	±2	120 < X ≤ 400	±10'
120 < X ≤ 400	±0.5			X > 400	±5'
400 < X ≤ 1000	±0.8				
X > 1000	±1.2				

BUTTON

Function	Description
RST	FX10 reset button
PROG	FX10 program button



LED Indicator

Function	Description
Power	LED turns on after power on
Program	LED turns on only when pressing the PROG button



INTERFACE OF POWER SUPPLY

External 12V power supply for GMSL3 cameras

- Part#: PJ-002A
- Number of Positions: 3
- Connector Type: Jack
- Inner Diameter: 2 mm
- Outer Diameter: 6.5 mm



INTERFACE (Reserved)

FPGA programming port

- Description: The port under the silicone plug is reserved for the FPGA programming.



● REVISION HISTORY

Revision	Description	Release Date
0.1	Initial draft.	18 Apr 2025
0.2	Changed the DeSerializer board and mechanical design.	16 Jul 2025
0.3	Replaced the 3D pictures.	21 Jul 2025

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