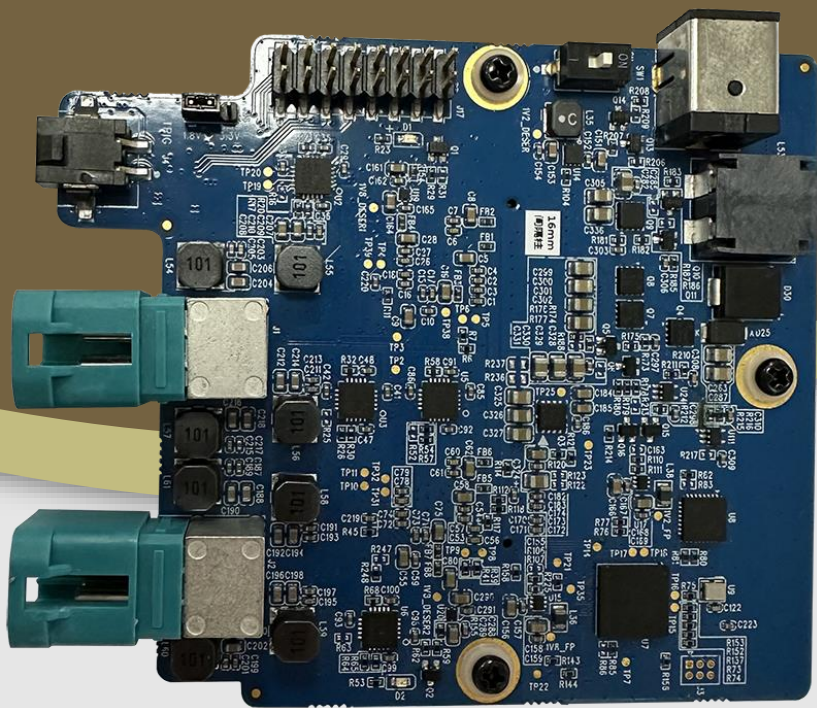




LI-JXAV-MAX96712-ADP-8CAM



Address:

910 Auburn Ct
Fremont, CA 94538
USA



Phone:

+1 (408)263-0988

Fax:

+1 (408)217-1960



Sales:

sales@leopardimaging.com

Support:

support@leopardimaging.com

TECHNICAL FEATURES

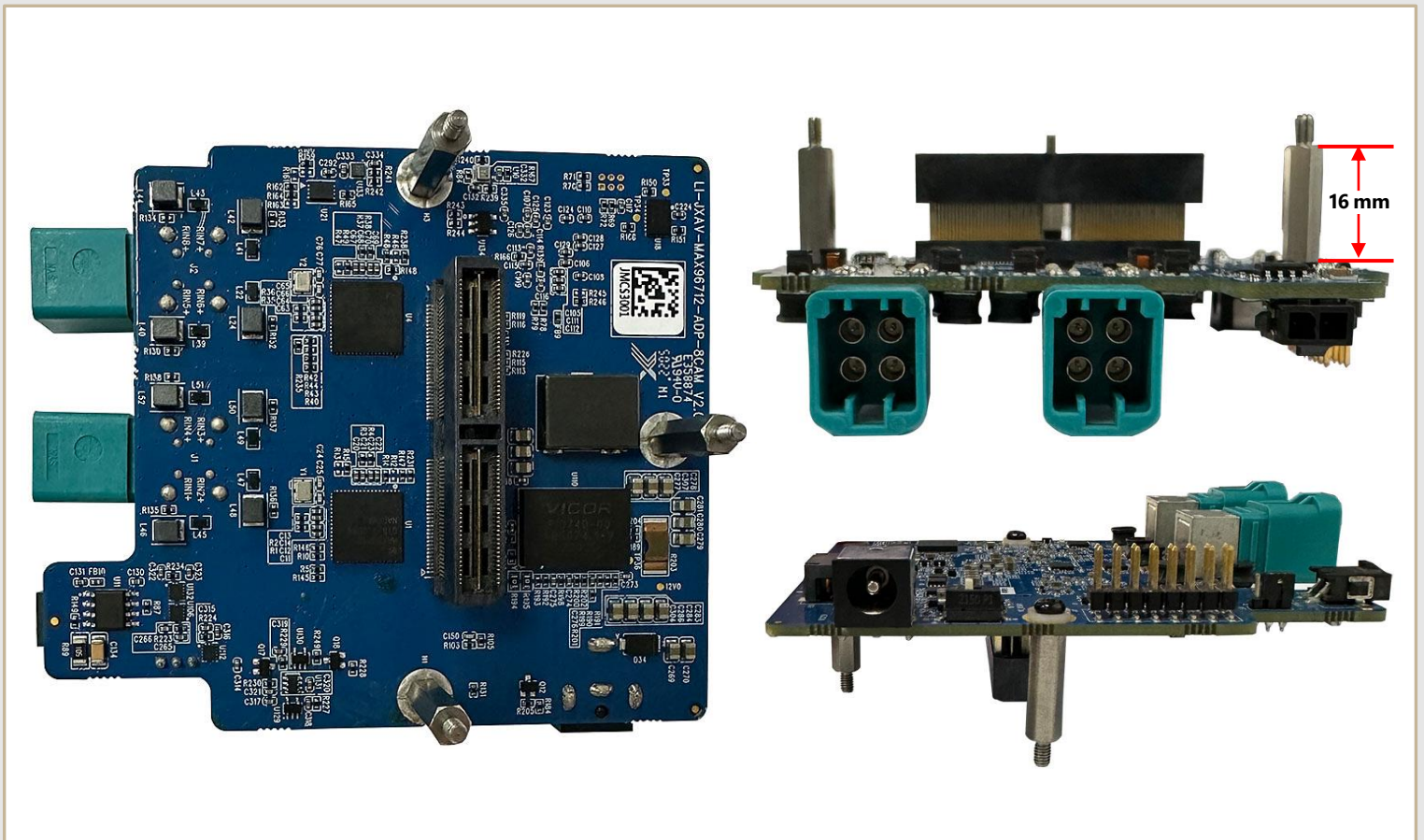
- Compatible with NVIDIA® Jetson AGX Xavier™ Developer Kit and Jetson AGX Orin™ Developer Kit

NOTE: LI-JTX1-SUB-ADPT is needed when connecting the LI-JXAV-MAX96712-ADP-8CAM to the developer kit.

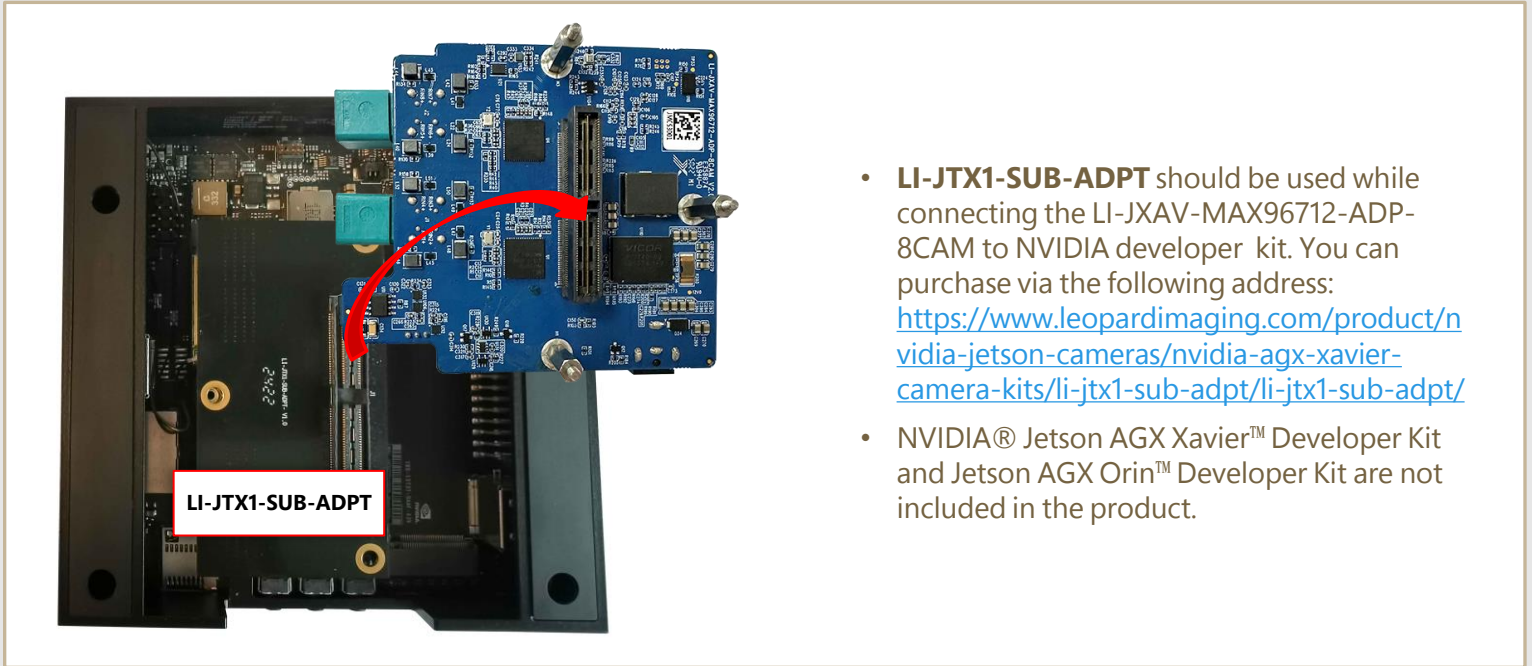
- Supports up to 8 GMSL2 cameras (Over Current Protection supported)

Mating connector to the board: 2298721-9

- Supports external 12 VDC power supply
- DeSerializer: Maxim MAX96712
- Operating temperature: -20°C ~ +60°C
- Weight: ~ 64 g
- Part#: **LI-JXAV-MAX96712-ADP-8CAM** (with 3 x 16 mm standoffs)



CONNECTION NOTE



- **LI-JTX1-SUB-ADPT** should be used while connecting the LI-JXAV-MAX96712-ADP-8CAM to NVIDIA developer kit. You can purchase via the following address: <https://www.leopardimaging.com/product/nvidia-jetson-cameras/nvidia-agx-xavier-camera-kits/li-jtx1-sub-adpt/li-jtx1-sub-adpt/>
- NVIDIA® Jetson AGX Xavier™ Developer Kit and Jetson AGX Orin™ Developer Kit are not included in the product.

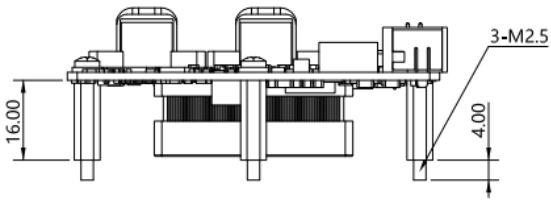
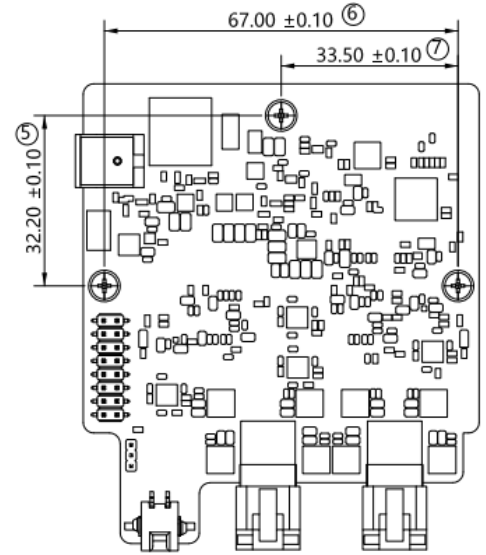
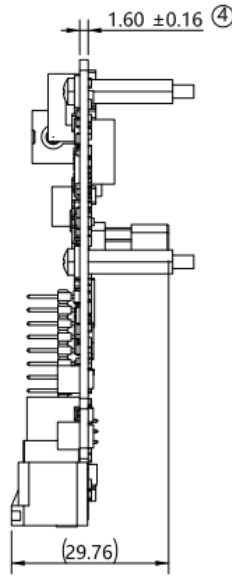
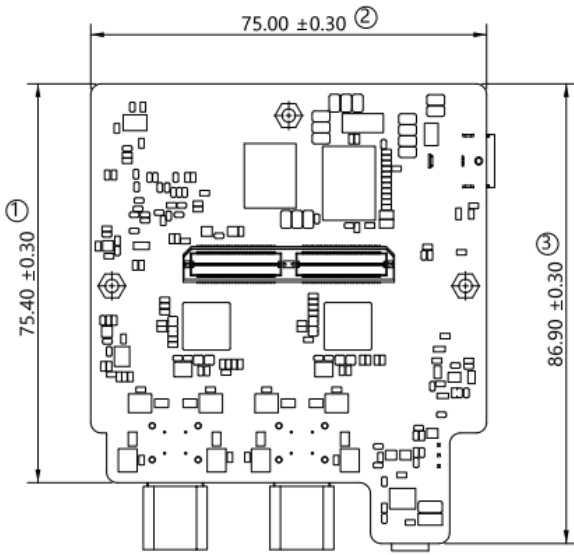
BOM DETAILS

#	Items	QTY
1	LI-JXAV-MAX96712-ADP-8CAM V2.0 board (with 3 x 16 mm standoffs)	1
2	12 VDC power supply	1

NOTE:

The 4-to-1 cable is not included. You can purchase via the following address: <https://www.leopardimaging.com/product/accessories/cables/li-fcb-4t1-ss-2m-np-t1/>

DIMENSIONS



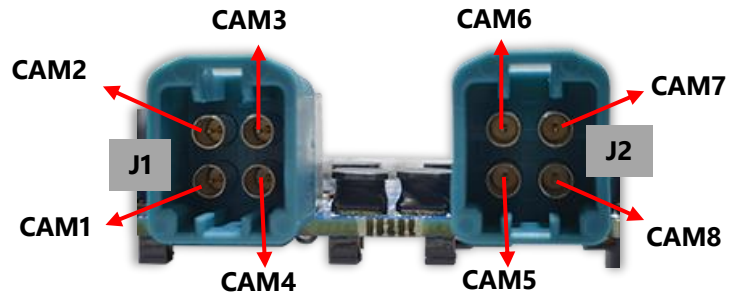
NOTE:

- ⊗ marked are important sizes.
- All materials are compliant with RoHS requirements.
- Other unmarked tolerances are ± 0.3 mm.

Unit: mm

INTERFACE J1 & J2 (4T1 Connector)

- Part#: 1206161400
- Number of Positions: 4
- Mating Connector Part#: 2298721-9



Boot Mode (SW1)

Status	Boot Mode
On	Buck boost: supports wide output voltage range (9 ~ 15 VDC)
Off (Default)	Bypass: supports 12V



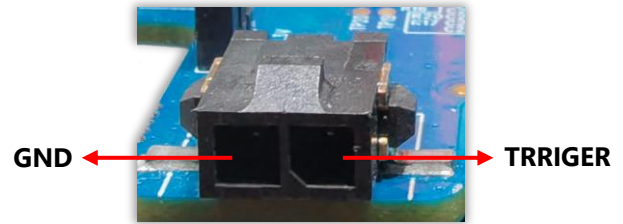
● INTERFACE J30 (DC PWR JACK)

- Part#: PJ-063AH
- Number of Positions: 2
- Connector Type: Jack
- Inner Diameter: 2 mm
- Outer Diameter: 6.5 mm



● INTERFACE J4 (External TRIG IN)

- Part#: 2-1445057-2
- Number of Positions: 2
- Pitch: 3.00 mm



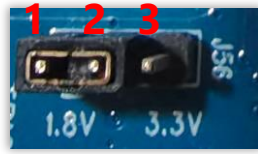
● Connector J5 (To Specific Xavier / Orin Carrier Board)

- Part#: QTH-060-02-L-D-A
- Number of Positions: 120
- Number of Rows: 2
- Pitch: 0.50 mm
- Compatible with **LI-AGO-CB V1.0** or **LI-AGX-CB V2.0**



INTERFACE J56 (Jumper for IO Power)

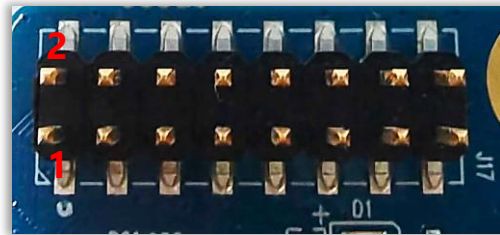
- Part#: M22-2010305
- Number of Positions: 3
- Number of Rows: 1
- Pitch: 2.00 mm



Position	IO Power
1-2	1V8 (Default)
2-3	3V3

IO CONNECTOR J17

- Part#: M20-8760842P
- Number of Positions: 16
- Number of Rows: 2
- Pitch: 2.54 mm
- VCCIO_GPIO: Depends on the IO power selected in above J56. By default, it is 1.8V.



Pin	Definition	Pin	Definition
1	VDDIO_GPIO	2	AGGA_LOCK_HEADER To MAX96712
3	MFP11_ACC1_INT1_HEADER	4	MFP6_GRYO2_INT3_HEADER
5	MFP12_GRYO1_INT3_HEADER	6	MFP5_ACC2_INT1_HEADER
7	MFP14_TEMP1_ALERT_HEADER	8	AGGA_ERR_HEADER
9	GND	10	FRSYNC_HEADER
11	GND	12	GND
13	N/A	14	N/A
15	N/A	16	N/A

- Xavier does not support FRAME_SYNC function.

NOTE:

- Pins from 2 ~ 8 are connected to MAX96712 MFP pins.
- FRAME_SYNC (Pin#10) direction:

Orin SOM Config	Pin#10 Direction	Note
PIN_G7 (GPIO13) = 0	Output	Driven by Pin_E59 (GPIO6) of Orin SOM
PIN_G7 (GPIO13) = 1	Input	To Pin_A62 (GPIO10) of Orin SOM

● REVISION HISTORY

Revision	Description	Release Date
1.0	First release.	11 Oct 2023

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